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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Eric Lee, Dave Cobert and Wanning Cao
Assignee: Integrated Device Technology, Inc.
Title: CONFORMAL SURFACE SILICIDE STRAP ON SPACER
(As amended)

Serial No.: 09/772,115
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R. K. K. K.
2/26/2002

ASSISTANT COMMISSIONER FOR PATENTS
Washington, D. C. 20231

Date: January 25, 2002

Sir:

RESPONSE TO FIRST OFFICE ACTION

Applicants submit the following remarks and amendments
in response to the Office Action dated October 25, 2001.

IN THE TITLE
Change the title to "Conformal Surface Silicide Strap
On Spacer".

IN THE CLAIMS
Replace Claim 8 with the following new Claim 8:

8. (Amended) A semiconductor structure comprising:
a semiconductor substrate;
a conductive element located over the
semiconductor substrate;
a dielectric spacer located adjacent to a
sidewall of the conductive element; and
a continuous silicide strap located over the
conductive element, the dielectric spacer and the